

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10067151	01/29/2002	716	16	2825	DO

****APPLICANTS:** Rupp Charles; Garverick Timothy; Arnold Jeffrey;

****CONTINUING DATA VERIFIED:**

THIS APPLN CLAIMS BENEFIT OF 60/265,303 01/30/2001

YES TO

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiners' initials		TD	STRE-003/01US
TITLE : Design methodology for merging programmable logic into a custom IC			
U.S. REPT. OF COMM. PAT. & TM.-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED			
		<table border="1"> <tr> <td>Total Claims</td> <td>Print Claim for U.S. Pat. & Trademark Office</td> </tr> </table>		Total Claims	Print Claim for U.S. Pat. & Trademark Office
Total Claims	Print Claim for U.S. Pat. & Trademark Office				
Assistant Examiner					
ISSUE FEE		DRAWING			
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn		
			Print Figs.		
Primary Examiner					
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner			
PREPARED FOR ISSUE					
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